



B/IFW

Attorney docket no. 16356.737 (DC-02355)
Customer No. 000027683

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Verdun, Gary J.

Serial No.: 09/707,585 ✓

Filed: November 6, 2000

For: PROCESSOR POWER STATE
TRANSITIONS USING SEPARATE
LOGIC CONTROL

§ Notice of Allowance date:
§ January 26, 2005
§ Art Unit: 2115
§ Examiner: Butler, Dennis
§ Confirmation No. 9642
§

LETTER TO THE OFFICIAL DRAFTSPERSON

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are ten (10) sheets of formal drawings for the above-identified patent application. Applicant respectfully requests approval.

Respectfully submitted,


James R. Bell
Registration No. 26,528

Dated: 4-26-05
HAYNES AND BOONE, L.L.P.
901 Main Street, Suite 3100
Dallas, Texas 75202-3789
Telephone: 512/867-8407
Fax: 214/200-0853

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

4/26/05



Nishi Pasarya